

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Group Art Unit: 2811
Osamu NAKAMURA et al.) Examiner: D. Kang
Serial No. 10/056,055) <u>CERTIFICATE OF MAILING</u> I hereby certify that this correspondence is
Filed: January 28, 2002	being deposited with the United States Postal Service with sufficient postage as First Class
For: SEMICONDUCTOR DEVICE AND) Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450,
MANUFACTURING METHOD OF) Alexandria, VA 22313-1450, on April 6, 2006
THE SAME	1 Cos Jichel

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the *Notice of Allowance and Fee(s) Due* dated January 6, 2006, submitted herewith are two sheets of corrected formal drawings consisting of Figures 24 and 25.

Respectfully submitted,

Eric J. Robinson Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C. PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789